



P.VENKATA LAVANYA

B.Tech, M.Tech, MISTE (Ph.D)

Electronics & Communication Engineering

AREAS OF INTEREST:

1. Embedded Systems
2. Digital and Image processing

EDUCATIONAL & PROFESSIONAL:

Academic Qualification:

- Pursuing **Ph.D (Signal & Image Processing)** in from **JNTUK**, Kakinada.
- **M.Tech** (Embedded Systems) from **ST.MARYS** College of Engineering & Technology, Hyderabad, 2007-2009.
- **B.Tech (ECE)** passed from **RVR & JC** Engineering College, GUNTUR, and 2000-2004.

Paper Publications:

Journals:

1. P.Venkata Lavanya, C.Venkata Narasimhulu, K.Satya Prasad:“ “A Combined approach of wavelet Thresholding and wavelet Bayesian Network models for Image Denoising” Scopus Journal –IJAER International Journal of Applied Engineering Research 2015. International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10 No.44 (2015).
2. P.Venkata Lavanya, C.Venkata Narasimhulu, K.Satya Prasad:“ “Image Denoising using Dual Tree Complex Wavelet Transform Based Thresholding Methods” in IJCRT International Journal of Creative Research Thoughts, ISSN:2320-2882, Volume 6, Issue 1 February 2018.

Conferences:

1. P.Venkata Lavanya, C.Venkata Narasimhulu: “ A Review on Different Techniques of Image Denoising” in Third National Conference on Latest Trends in Signal Processing ,VLSI and Embedded systems, 2014.
2. P.Venkata Lavanya, C.Venkata Narasimhulu, K.Satya Prasad:“Transformations Analysis for Image Denoising Using Complex Wavelet Transform” 2017 International Conference on Innovations in Information, Embedded and Communication Systems (ICIIECS) Included in IEEE Xplore with DOI: 10.1109/ICIIECS.2017.8276155

Participated:

- Participated a two day workshop on “Latest advances in Electronics and communication systems” held at TKRCET, in March-2013.
- Participated a three day workshop on “PCB design” held at TKRCET, in September 2013.
- Participated a Two day workshop on “Image Processing using MATLAB” held at TKRCET, in January 2014.
- Participated a Two day workshop on “Digital Image Processing using MATLAB “, in January , 2014.
- Participated in 3 day interactive workshop on “Intel Atom Embedded Training Program-3” held at JNTUH in March 2014.
- Participated one week FDP on “Modern teaching trends in Scientific and Technical Education” in July, 2014.
- Participated a Two day workshop on “Embedded and VLSI Design”, in August, 2014.
- Participated FDP on “Research awareness in Advanced VLSI Design” in September, 2014.
- Participated FDP on “Teaching Pedagogy, Programme Education objectives” in 2014.
- Participated in 2 day workshop on “Digital IC Design” in June, 2014.
- Participated Training Programme on “Data Networking, Routing and Switching “ in September, 2015.
- Participated in 1 day workshop on “Research Trends in VLSI & Embedded Systems “ in November, 2016.
- Participated in 3 day workshop on “Robotics” in July, 2016.
- Participated in 2 day workshop on “Home Automation with IoT using Raspberry Pi” in August, 2017.
- Participated in 2 day workshop on “Introduction to Python” held in October,2017.
- Participated in 2 day workshop on “PCB Design and Fabrication” in October,2017.
- Participated in workshop on ARM Cortex M3 Architecture.
- Participated a Two day workshop on “Digital Image Processing using MATLAB “, in January , 2014.

Subjects Taught:

DIP, STLD, MPMC, EDC, ES, RS, DSP, MC&A, POC