



## S. SAI SREE ANDAL

B.TECH, M.TECH

ASSISTANT PROFESSOR

Electronics & Communication Engineering

### Areas of interest:

1. VLSI
2. EMBEDDED SYSTEMS
3. COMMUNICATIONS

### EDUCATIONAL & PROFESSIONAL:

#### Academic Qualification:

- B.Tech in Electronics and Communication Engineering, ST. Mary's College of Engineering and Technology (2010).
- M.tech in VLSI System Design (ECE), Teegala Krishna Reddy Engineering College (2013).

#### Paper publications:

1. M.Jhansi Rani, S. Sai Sree Andal,P. Kalyani.Carry Select Adder design using Brent Kung Adder, Volume 4 Issue 6, IJETA ,2017.
2. B. Swapna, S. Sai Sree Andal.Implementation of Scientific Calculator Using CORDIC Algorithm on FPGA, Volume No: 3 , Issue No: 12 (November),PAGES 34-36,yuva engineers,2014.
3. S. Sai Sree Andal, N. Aravind, FPGA Modeling Of Neuron for Future Artificial Intelligence Applications, Vol, 03,Issue, 7,PAGES 40-46,IJCER,2013.

#### Participated:

- Participated a two day workshop on "Latest advances in Electronics and communication systems" held at TKRCET, in March-2013.
- Participated a three day workshop on "PCB design" held at TKRCET, in September 2013.
- Participated a Two day workshop on "Image Processing using MATLAB" held at TKRCET, in January 2014.

- Participated a Two day workshop on “Digital Image Processing using MATLAB “, in January , 2014.
- Participated in 2 day workshop on “Embedded System Design Using Atmel 8 bit and 32 bit MCU” held at JNTUH in February 2014.
- Participated in 3 day interactive workshop on “Intel Atom Embedded Training Program-3” held at JNTUH in March,2014.
- Participated in 2 day workshop on “Digital IC Design” in June, 2014.
- Participated one week FDP on “Modern teaching trends in Scientific and Technical Education” in July, 2014.
- Participated FDP on “Research awareness in Advanced VLSI Design” in September, 2014.
- Participated FDP on “Teaching Pedagogy, Programme Education objectives” in 2014.
- Participated a Two day workshop on “Embedded and VLSI Design”, in August, 2014.
- Participated Training Programme on “Data Networking, Routing and Switching “ in September, 2015.
- Participated a Three day workshop on “ARM based Embedded Processing “ in August, 2015.
- Participated in 3 day workshop on “Robotics” in July, 2016.
- Participated in 2 day workshop on “Home Automation with IoT using Raspberry Pi” in August, 2017.
- Participated in 2 day workshop on “PCB Design and Fabrication” in October,2017
- Participated in 2 day workshop on “Introduction to Python” held in October,2017.
- Participated FDP on “Simulation of Wireless sensor Networks using NS2” held at SNIST in November , 2017.
- Participated FDP on “Analog IC Design - Hands on Practice using Cadence Design Flow" held at JNTUH, in April 2019.

#### Teaching:

Microprocessors and Microcontrollers, Analog Communications, Electronic Devices and Circuits, Embedded Systems, Principles of communications and computer networks.

#### Positions held at TKRCET:

- Department Library Incharge for ECE department.