



A.THEJA

B.Tech, M.Tech

ASSOCIATE PROFESSOR

Electronics & Communication Engineering.

Areas of interest:

1. VLSI Design.
2. Signal Processing.

Academic Qualification:

- M.Tech in VLSI System Design (ECE), TKR College of Engineering & Technology (2012).
- B.Tech in Electronics and Communication Engineering, Srinivas Reddy Institute of Technology (2008).

Professional Qualification:

- Qualified in UGC-NET examination for Lectureship/Assistant Professor in June, 2014 & December, 2014.
- Qualified in GATE-2007, 2009 (AIR-1501), 2010, 2011, 2012, 2013, 2014.
- Got the “Best Faculty” award at TKRCET in 2016.

Professional Experience:

- Associate Professor, TKR College of Engineering & Technology (2017-Till date).
- Assistant Professor, TKR College of Engineering & Technology (2011-17).
- Lecturer, Kamareddy Engineering College (2008-09).

Paper publications:

1. *Varun Kumar Jagini, Tirumala Pooja, A.Theja, "WIRELESS ELECTRONIC SPEAKING SYSTEM FOR IMPAIRED PEOPLE"*, Volume IX, Issue IV, International Journal of Management, Technology And Engineering, APRIL/2019
2. Venkatesh.R, A.Theja, "*DESIGN OF HIGH SPEED MULTIPLIER USING MULTI OPERAND ADDER TREES*" Volume 3, Issue 4, OCT 2014 in IJRAET.
3. K.Srujana, A.Theja, Dr.D.Nageshwar Rao "*OPTIMIZING TECHNIQUE FOR THE IMPLEMENTATION OF FIR FILTER USING MULTIPLE CONSTANT MULTIPLICATIONS*" Volume 04, Issue 17, December 2017 in IJR.

Participated:

- Participated a 3 Day FDP on “Designing with ZYNQ SOC and its applications” organized by the JNTUH-CEH in October, 2019.

- Participated one week FDP on “Advanced CMOS VLSI Design” organized by the E & ICT Academy, NIT Warangal at Dept. of ECE” in December, 2018.
- Participated in the “AWR Design Forum 2018 RF/Microwave Design & Simulation” workshop, by National Instruments (NI) in September, 2018.
- Participated one week FDP on “Recent Trends in VLSI Design and its Applications” organized by the E & ICT Academy, NIT Warangal at Dept. of ECE, S.R.K.R. Engineering College” in November, 2017.
- Participated workshop on “Embedded systems and IOT- the opportunities” at ISM University in November, 2017.
- Participated in the “AWR Design Forum 2017 RF/Microwave Design & Simulation” workshop, by National Instruments (NI) in September, 2017.
- Participated one week FDP on “Analog and Digital IC design-using Cadence tools” in November, 2015 at CMRCET (Autonomous), Hyderabad.
- Participated FDP on “Research awareness in Advanced VLSI Design” in September, 2014.
- Participated workshop on “Embedded and VLSI design” in August, 2014.
- Participated one week FDP on “Modern teaching trends in Scientific and Technical Education” in July, 2014.
- Participated in the workshop on “Digital IC design” in June, 2014.
- Participated a Two day workshop on “Image Processing using MATLAB” held at TKRCET, in January 2014.
- Participated a three day workshop on “PCB design” held at TKRCET, in September 2013.
- Participated a two day workshop on “Latest advances in Electronics and communication systems” held at TKRCET, in March-2013.

Organized:

- Organized and attended workshop on “PCB design and Fabrication” in October, 2017.
- Organized and attended workshop on “Home Automation with IOT using Raspberry Pi” in August, 2017.
- Organized and attended workshop on “ARM based Embedded Processing” in August, 2015.
- Organized and attended workshop on “Data Networking, routing & switching” in August, 2015.

Teaching:

At UG-Level:

Electronic Devices and Circuits, Signals and Systems, Pulse and Digital Circuits, Antennas and Wave Propagation, Digital Signal Processing, VLSI Design, Control Systems Engineering, Digital Design through Verilog HDL, Digital Signal Processors and Architectures, Signals and stochastic process.

Positions held at TKRCET:

- Placement Coordinator for ECE department (2016-17).
- Workshop Coordinator for ECE department (2014-19).
- Project Coordinator at Department level.
- Member, Autonomous Syllabus framing.
- Department Time-table in charge.

Projects guided:

- 20 Projects guided at UG-Level.
- 4 Projects guided at PG-Level.