



A. Vikas

B.Tech., M.Tech.,(PhD)

Associate Professor

Electronics & Communication Engineering

Areas of interest:

1. VLSI
2. Communications

Academic Qualification:

- Pursuing PhD in ECE (2020-), National Institute of Technology, Patna.
- M.Tech in VLSI System Design (ECE), TKR College of Engineering & Technology (2012).
- B.Tech in Electronics and Communication Engineering, Srinivas Reddy Institute of Technology (2008).

Professional Qualification:

- Qualified in UGC-NET examination for Lectureship/Assistant Professor in June, 2013.
- Qualified in GATE-2011, 2012.
- Got the “Best Faculty” award at TKRCET in 2016.

Professional Experience:

- Associate Professor, TKR College of Engineering & Technology (2016-Till date).
- Assistant Professor, TKR College of Engineering & Technology (2011-16).
- Lecturer, Kamareddy Engineering College (2008-09).

Paper publications:

1. A.Theja, A.Vikas et al., *Forward M Ary Hypothesis Testing Based Detection Approach For Passive Radar*, Volume IX, Issue IV, APRIL/2019, IJMATES, pages 4031-4035.
2. A.Vikas, Y.Priyanka, A.Deepika, *A Novel Design And Implementation Of Hybrid LUT/Multiplexer For FPGA Logic Architectures*, VOL 4, ISSUE 17, 2017.
3. Dr.D.Nageshwer rao B.soumyasree, A.Vikas, *An Efficient VLSI Architecture for Montgomery Modular Multiplication*, VOL 4,ISSUE 4, PAGES 130-137, IJARTET, 2017.
4. K Thriveni, A Vikas, DRD Nageshwarrao, *Area Delay Efficient FM0/Manchester Encoding Using SOLS Technique for Communication*, VOL 4, ISSUE 5, PAGES 279-285, IJEET, 2017.
5. N VENNELA, A VIKAS, *FPGA Implementation of Efficient Modified Booth Encoder Multiplier for Signed and Unsigned Numbers*, VOL 2, ISSUE 1, PAGES 10-15, IJRAET, 2014.

Chapters Written: 1 (Accepted)

- Written a Chapter on “FinFET Process Technology for RF and Millimeter-Wave Application”, Scrivener Publishing partnership with Wiley, 2021.

NPTEL Courses:

- Got a certificate from NPTEL for completing 12-week course in “Microelectronics: Devices to Circuits”, 2019.

Participated:

- Participated in Five-Day Online FDP – cum - STTP on “Simulation, Modelling and Application of Advanced Semiconductor Devices” from 05 - 09 July, 2021, organized by the Department of Electronics Engineering, Harcourt Butler Technical University, Kanpur, Uttar Pradesh, India - 208002.
- Completed a “5-weeks online employability enhancement IEEE summer school on VLSI”, at Department of ECE, IIITD from 07.06.2021 to 10.07.2021.
- Participated and completed the Faculty Development Programme on “Next-Generation Trends For Nanotechnology in CMOS, Photonics, and Neuromorphic Computation” organized by Department of Electronics & Communication Engineering, NIT Patna under Electronics and ICT Academy, NIT Patna, held from 25th –30th January, 2021 in online mode under the “Scheme of financial assistance for setting up of Electronics and ICT Academies” by the Ministry of Electronics and Information Technology (MeitY), Government of India.
- Participated in a Three-Day Faculty Development Program on “Mixed Signal VLSI Circuit Design” Conducted by Anurag Group of Institutions (AGI), Hyderabad from 25th to 27th January 2021 under Equity Action Plan, TEQIP-III JNTUH, Hyderabad.
- Attended and participated in the five days’ online workshop on “Recent Trends in VLSI Devices/Circuits and Applications” held at MNIT Jaipur during October 1-5, 2020.
- participated in the CEP Course on “Modern Trends and Development in Semiconductor Microelectronics” conducted by IIT Patna during 09th October 2020 -11th October 2020.
- Attended the International Workshop on “Accelerate Research”, conducted online during 22nd – 26th, September 2020 by Anurag group of Institutions, Hyderabad.
- Attended one-week Faculty development program on “Analog IC Design”, Organized by IEEE CAS/EDS Chapter at Muffakham Jah College of Engineering and Technology, Hyderabad in December-2019.
- Attended a Two-day workshop on “Analog Integrated Circuit Design (Concept to Reality)” organized by IEEE CAS/EDS chapter in collaboration with AMS Semiconductors and Anurag Group of Institutions, Hyderabad in November,2019.
- Participated one-week FDP on “Advanced CMOS VLSI Design” organized by the E & ICT Academy, NIT Warangal at Dept. of ECE” in December, 2018.

- Participated in the “AWR Design Forum 2018 RF/Microwave Design & Simulation” workshop, by National Instruments (NI) in September, 2018.
- Participated one-week FDP on “Recent Trends in VLSI Design and its Applications” organized by the E & ICT Academy, NIT Warangal at Dept. of ECE, S.R.K.R. Engineering College” in November, 2017.
- Participated workshop on “Embedded systems and IOT- the opportunities” at ISM University in November, 2017.
- Participated in the “AWR Design Forum 2017 RF/Microwave Design & Simulation” workshop, by National Instruments (NI) in September, 2017.
- Participated one-week FDP on “Analog and Digital IC design-using Cadence tools” in November, 2015 at CMRCET (Autonomous), Hyderabad.
- Participated FDP on “Research awareness in Advanced VLSI Design” in September, 2014.
- Participated workshop on “Embedded and VLSI design” in August, 2014.
- Participated one-week FDP on “Modern teaching trends in Scientific and Technical Education” in July, 2014.
- Participated in the workshop on “Digital IC design” in June, 2014.
- Participated a Two-day workshop on “Image Processing using MATLAB” held at TKRCET, in January 2014.
- Participated a three-day workshop on “PCB design” held at TKRCET, in September 2013.
- Participated a two-day workshop on “Latest advances in Electronics and communication systems” held at TKRCET, in March-2013.

Organized:

- Organized and attended workshop on “PCB design and Fabrication” in October, 2017.
- Organized and attended workshop on “Home Automation with IOT using Raspberry Pi” in August, 2017.
- Organized a one-day Industrial visit to “BSNL-RTTC”, Gachibowli, Hyderabad in March,2015.
- Organized and attended workshop on “ARM based Embedded Processing” in August, 2015.
- Organized and attended workshop on “Data Networking, routing & switching” in August, 2015.

Teaching:

At UG-Level:

VLSI Design, CMOS Analog IC Design, Optical Communications, Switching Theory and Logic Design, Analog Communications, Control Systems Engineering, Electronic Devices and Circuits, Radar Systems.

At PG-Level:

System on Chip Architectures, Embedded Real Time Operating Systems.

Positions held at TKRCET:

- Placement Coordinator for ECE department (2016-17).
- Workshop Coordinator for ECE department (2014-19).
- ARIIA Coordinator for ECE department.
- Project Coordinator at Department level.
- Department Fest (ECTA) Coordinator.
- Member, Institute Industry Interaction Committee.
- Member, Autonomous Syllabus framing.
- Department Time-table in-charge.
- Coordinator for Website updating.

Projects guided:

- 22 Projects guided at UG-Level.
- 4 Projects guided at PG-Level.

Contact:

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