



TKR COLLEGE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS)

[Sponsored by TKR Educational Society, Approved by AICTE, Affiliated to JNTUH]
Accredited by NBA & NAAC with 'A+' Grade



EXAMINATION BRANCH
External Time Table – 2022 Admitted Batch
B .TECH V SEMESTER – R22 REGULATION REGULAR EXAMINATIONS – DECEMBER -2024
ACADEMIC YEAR – 2024-25 **TIME: AN: 2.00 PM TO 5.00 PM**

BRANCH	DATE, SESSION & DAY					
	23.12.2024 MONDAY	28.12.2024 SATURDAY	31.12.2024 TUESDAY	03.01.2025 FRIDAY	06.01.2025 MONDAY	08.01.2025 WEDNESDAY
CE	Water Resources Engineering-I	Design of Reinforced Concrete Structures	Structural Analysis-I	Transportation Engineering	Solid and Hazardous Waste Management	***
EEE	Power Electronics	Electrical Measurements and Instrumentation	IOT Applications in Electrical Engineering	Digital signal processing	Electrical Engineering Materials	***
ECE	Control Systems	Microprocessors and Microcontrollers	Business Economics and Financial Analysis	Data Communications and Computer Networks	Python Programming	***
CSE	Fundamentals of Management	Operating Systems	Formal Languages and Automata Theory	Data Warehousing and Data Mining	Artificial Intelligence	Fundamentals of Data Science
CSE (AI&ML)	Automata Theory and Compiler Design	Operating Systems	Machine Learning	DevOps	Artificial Intelligence	Object Oriented Analysis & Design
CSE (DS)	Automata Theory and Compiler Design	Business Intelligence	Machine Learning	DevOps	Distributed Database	Internet of Things
IT	Fundamentals of Management	Computer Networks	Fundamentals of IOT	Quantum Computing	Biometrics	Fundamentals of Data Science

NOTE: ANY CORRECTIONS OR CHANGES IN THE TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY

If any Students have Substitute subjects, the same day with regular examinations will be conducted.

Date: 23.10.2024

Additional Controller of Examinations
TKR College of Engineering & Technology
(AUTONOMOUS)
Medbowli, Meerpet, Hyderabad- 500097.

Controller of Examinations
TKR College of Engineering & Technology
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PRINCIPAL
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EXAMINATION BRANCH
External Time Table – 2021 & 2020 Admitted Batch
B.TECH V SEMESTER – R20 REGULATION SUPPLEMENTARY EXAMINATIONS – DECEMBER -2024
ACADEMIC YEAR – 2024-25 TIME:AN: 2.00 PM TO 5.00 PM

BRANCH	DATE, SESSION & DAY					
	23.12.2024 MONDAY	28.12.2024 SATURDAY	31.12.2024 TUESDAY	03.01.2025 FRIDAY	06.01.2025 MONDAY	08.01.2025 WEDNESDAY
CE	Fundamentals of Management	Structural Analysis-II	Design of Reinforced Cement Concrete Structures	Soil Mechanics	Transportation Engineering	Environmental Impact Assessment
EEE	Power Electronics	Electrical Measurements and Instrumentation	Electrical Machine Design	Power System-II	--	Computer Networks
					--	Smart Grid Technologies
ME	Dynamics of Machinery	Design of Machine Members-I	Thermal Engineering-II	Operations Research	Instrumentation and Control Systems	Automobile Engineering
ECE	Business Economics and Financial Analysis	Python Programming	Digital Communications	Microprocessor and Microcontrollers	Control Systems	Digital Design through Verilog HDL
						Image Processing & Pattern Recognition
						Introduction to Embedded Systems
CSE	Object Oriented Analysis and Design	Python Programming	Computer Networks	Compiler Design	Artificial Intelligence	Environmental Impact Assessment
					Distributed Databases	Embedded System Design
IT	Object Oriented Analysis and Design	Python Programming	Computer Networks	Compiler Design	Principles of Electronic Communications	Software Engineering
CSE (AI&ML)	Introduction to AI & Neural Networks	Python Programming	Natural Language Processing	Introduction to Machine Learning	Distributed Databases	Smart Grid Technologies
CSE (DS)	Fundamentals of Management	Web Technologies	Machine Learning	Compiler Design	Distributed Databases	Software Engineering

Additional Controller of Examinations
TKR College of Engineering & Technology
Date: 23/10/2024
Medbowli, Meerpet, Hyderabad-500097
ACE

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Examination Branch
External Time Table- 2019 & 2018 Batch
V SEMESTER –R18 REGULATION SUPPLYMENTARY EXTERNAL EXAMINATIONS- DECEMBER 2024
ACADEMIC YEAR- 2024-25 **TIME: (AN: 2.00 PM TO 5.00 PM)**

BRANCH/ DATES/ DAY	23.12.2024 MONDAY	28.12.2024 SATURDAY	31.12.2024 TUESDAY	03.01.2025 FRIDAY	06.01.2025 MONDAY	08.01.2025 WEDNESDAY
CIVIL	Structural Analysis-II	Design of Reinforced Cement Concrete Structures	Transportation Engineering	Concrete Technology	Fundamentals of Management	--
EEE	Power Systems-I	Power Electronics	Microprocessors and Micro Controllers	Electrical Machine Design	Non-Conventional Power Generation	--
				Digital Signal Processing		
ME	Design of Machine members-I	Thermal Engineering-I	Machine Tools	Automobile Engineering	Fundamentals of Management	Engineering Metrology
ECE	Electromagnetic Theory and Transmission Lines	VLSI Design	Digital Signal Processing	Digital Communications	OOPs Through JAVA	--
					Operating Systems	
CSE	Operating Systems	Computer Networks	Theory of Computation	Design and Analysis of Algorithms	Fundamentals of Management	Web Technologies
IT	Operating Systems	Computer Networks	Data Warehousing & Data Mining	Design and Analysis of Algorithms	Fundamentals of Management	Linux Programming

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2. ANY CHANGES IN ELECTIVE SUBJECTS INFORM IMMEDIATELY TO THE CONTROLLER OF EXAMINATIONS

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Examination Branch External Time Table- 2017 Batch

III B. Tech – I SEMESTER –R17 REGULATION-SUPPLYMENTARY EXTERNAL EXAMINATIONS- DECEMBER 2024

ACADEMIC YEAR- 2024-25

TIME: (AN: 2.00 PM TO 5.00 PM)

BRANCH/ DATES/ DAY	23.12.2024 MONDAY	28.12.2024 SATURDAY	31.12.2024 TUESDAY	03.01.2025 FRIDAY	06.01.2025 MONDAY
CIVIL	Concrete Technology	Design of Reinforced Concrete Structures	Water Resources Engineering-I	Transportation Engineering	Construction Equipment and Materials
EEE	Electrical Measurements & Instrumentation	Power Systems – II	Microprocessors and Microcontrollers	Fundamentals of Management	Digital Signal Processing
ME	Design of Machine members-I	Thermal Engineering-I	Metrology and Machine tools	Fundamentals of Management	Automobile Engineering
ECE	Electromagnetic Theory and Transmission Lines	Linear & Digital IC Applications	Digital Communications	Fundamentals of Management	Digital Design Using Verilog HDL
CSE	Computer Networks	Principles of Programming Languages	Design and Analysis of Algorithms	Compiler Design	Software Engineering
IT	Design and Analysis of Algorithms	Data Communication & Computer Networks	Software Engineering	Fundamentals of Management	Information Security

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