



K.SANTOSHI
M.TECH
ASSISTANT.PROFESSOR
Electronics & Communication Engineering

Area of Interest:

- 1. SWITCHING THEORY AND LOGIC DESIGN**
- 2. VLSI**
- 3. DIGITAL IMAGE PROCESSING**

Details Of Education, Teaching

Education Qualification

- **M. Tech (DECS)** from AITAM, Autonomous Affiliated to the University JNTU, Kakinada in the year 2015 with 9.0 CGPA (approximately 85.50%).
- **B. Tech (ECE)** from AITAM, Affiliated to the University JNTU, Kakinada in the year 2012 with 75.46%.

Employment record

- Working as Assistant Professor in TKR Engineering College, Meerpet from January 2023 to till date (2months experience).
- Worked as an Assistant Professor in Marri Laxman Reddy Institute of Technology and Management (MLRITM), Dundigal from Febraury 2022 to December 2022 (10 months experience).
- Worked as an Assistant Professor in Sarada Institute of Science Technology and Management

(SISTAM), Srikakulam from March 2018 to July 2021 (03 years experience).

- Worked as an Assistant Professor in Aditya institute of technology and management (AITAM), Srikakulam from April 2015 to May 2017 (02 years experience).

Workshops and Development Programs attended in the recent past:

● **Workshops:**

- Attended a 2-day workshop on “MATRIX Laboratory” organized by the department of ECE, AITAM College, Tekkali.

● **FDP’s:**

- Attended a one-week Faculty Development Programme on "**Amazon Web Services**" during the period of 22nd August 2022 to 27th August 2022 Organized by Institute of Aeronautical Engineering in collaboration with BRAINOVISION SOLUTIONS INDIA PVT.LTD & ALL INDIA COUNCIL FOR TECHNICAL EDUCATION – AICTE.
- Attended a one Week Virtual International Faculty Development Programme on Smart Tools and Methodologies for Academic Research for Academic Research organized by the Department of ECE, Mahatma Gandhi Institute of Technology, Hyderabad in association with Swecha during 01 – 05 August 2022.

● **Certification:**

- Achieved Elite certificate in NPTEL programme for the successful completion in “**DIGITAL SYSTEM DESIGN**”, organized by Indian Institute of Technology, Madras in April 2022.

LIST OF PUBLICATIONS

International Journals:

- Published a Paper in an international journal on “**Performance Improvement in Noise Reduction Based On Double Density Dual Tree Complex Wavelet Transform**” in IJAER,2015.

Subjects Handled (UG Level):

- Pulse Digital Circuits
- Switching theory and logic design
- Digital logic design
- VLSI