



K.SUSHMA
ASSISTANT.PROFESSOR
Electronics & Communication Engineering

Area of Interest:

- 1. EMBEDDED SYSTEMS**
- 2. DIGITAL ELECTRONICS**

Details Of Education, Teaching

Education Qualification

- M.Tech (VLSI System Design) from Nova college of Engineering and Technology, affiliated to JNTU during the year 2012.
- B.Tech (ECE) from St.Mary's college of Engineering and Technology, affiliated to JNTU during the year 2006.
- Intermediate from Narayana Jr.College, during the year 2002.
- S.S.C. from St.Andrew's High School, during the year 1999.

Employment record

- Worked as an **ASSISTANT PROFESSOR** in KMIT, from 24-04-2015 to 30-11-2020.
- Worked as an **ASSISTANT PROFESSOR** in SVIT from 02-01-2012 to 30-04-2015.
- Working as Assistant Professor in TKR Engineering College, Meerpet from February 2023 to till date

Faculty Development Programs/Workshops/seminars:

- Participated and successfully completed one week AICTE & ISTE approved FDP on Channel Coding Techniques organized by GNITS.
- Participated in a Two Day FDP on "Research Methodology" in SVIT.
- Participated in one week FDP on "Advanced Front End VLSI Systems Prototyping using FPGAs" in KMIT

Subjects Handled (UG Level):

- Digital Logic Design(DLD)
- Switching Theory and Logic design(STLD)

- Microprocessors and Microcontrollers(MPMC)
- Computer Organization(CO)
- Electronic Devices and circuits(EDC)

Certifications:

- Digital Electronic circuits NPTEL Certification
- Python Certification from Course era.

JNTUH Ratification:

- Ratified as Assistant Professor in 2015 from JNTUH.