



Dr MAHESH BE., M.Tech., Ph.D., MISTE
Professor
Electronics & Communication Engineering

Areas of interest: Microprocessors, Microcontrollers
Embedded systems
Image processing
Digital systems

1. Educational qualifications

S. No	COURSE	University	YEAR OF PASSING
1	PhD (Image processing)	JNTU, Anantapur	2014
2	M. Tech (Digital Systems Computer Electronics)	JNTU, Hyderabad	2005
3	B.E (ECE)	Gulbarga University, Gulbarga, Karnataka	1996

2. Experience: 19+ years

S. No	Position	Name Of The Organization	Period
1	Professor	TKR College of Engg. & Tech	November 2020 to till date
2	Professor	Swami Vivekananda Institute of Technology, Secunderabad	April-2018 to Sep-2020
3	Professor & HOD	Universal college of engineering Guntur Dist.	Mar-2016 to Apr-2018
4	Associate Professor & Academic Section In charge	MITS, Institute Of Tech, & Science Madanapalle, Chittoor Dist	June-2007 to Feb- 2016
5	Associate Professor & HOD	GATES Institute of Technology Gooty, Anantapur Dist	July 2005 to May 2007
6	Asst. Professor	ALFA college of Engg & Tec Allagadda, Kurnool Dist	June 2003 to June 2005

3. Paper Publications

No. of Publications: 15

<http://scholar.google.co.in/citations?hl=en&user=zeZU0pkAAAAJ>

4. Conferences

4A. International Conferences: 03

[1] **Mahesh** and Dr. M.V. Subramanyam “Automatic Image Mosaic System Using Steerable Harris Corner Detector” International Conference on Machine Vision and Image Processing (MVIP12), 14th and 15th December 2012, PSG Tech Coimbatore. pp 87-91. DOI: [10.1109/MVIP.2012.6428767](https://doi.org/10.1109/MVIP.2012.6428767)

[2] **Mahesh** and Dr. M.V. Subramanyam “Feature Based Image Registration Using Steerable Filters and Harris Algorithm”. Fourth International Conference ARTCom - 2012, p. 95 – 99 Bangalore Oct 19-20, 2012 Proceedings DOI: [10.1049/cp.2012.2503](https://doi.org/10.1049/cp.2012.2503) ISBN: 978-1-84919-929-2 IET Digital library.

[3] **Mahesh** and Dr. M.V. Subramanyam “Automatic Feature Based Image Registration Using SIFT Algorithm” Third International Conference on Computing, Communication and Networking Technologies (ICCCNT'12)2012, Coimbatore.26-27thJuly2102. DOI: [10.1109/ICCCNT.2012.6396024](https://doi.org/10.1109/ICCCNT.2012.6396024)

4B. National Conferences: 05

[4] **Mahesh.**, et al “Torn paper combining using Binary Image Processing” A two day National conference on wireless Communication systems” pp-239-246, on 27th-28th January, 2016, at MITS, madanapalle.

[5] **Mahesh.**, et al “Comparative study on SIFT Matching algorithm for Image Mosaicing ” National conference on Communication & Soft Computing” on 9th August 2012, K.S. Institute of Technology, Bangalore.

[6] **Mahesh** and Y.Murali “Image Mosaic Using SURF” A one day National conference on Embedded systems & Applications on 20th March 2012, MITS, Madanapalle.

[7] **Mahesh.**, et al “Optimization and characterization of image Registration Using Wavelet Modulus Maxima method ” Two day National conference on Applications of analog & Digital devices conducted on 6th-7th of October 2010, Dr M G R University, Chennai.

[8] **Mahesh.**, et al “Compare and Improvement of wavelet Based Image Fusion” National conference on Emerging Trends in Electronics & communication on 6th February 2010, St. Ann’s college of Engineering & Technology, Chirala.

5. Work Shop/Seminar Attended

[9] One week FDP on “Machine learning with python” from 26th Nov to 1st Dec 2018, NIT Warangal & SVIT, Secuuderababd

[10] One day workshop on “Complementary Domains- VLSI & Embedded Systems (CDVES-13)” on 30th April 2013, JNTUA college of Anantapur

[11] A three day International workshop on “Wireless Network” Indo-Us Engineering Faculty leadership institute from 18th to 20th June 2013, at MITS, madanapalle

[12] One week International workshop on “Data Communications IC Design” Indo-Us Engineering Faculty leadership institute from 25th to 29th June 2012, at MITS, madanapalle

[13] One day National workshop on “Digital Image Processing” at MITS, madanapalle on 30th Sep 2011

[14] One week International workshop on “Embedded Computing System: From Processor Design to Code Optimization” Indo-Us Engineering Faculty leadership institute from 20th to 24th June 2011, at MITS, madanapalle.

[15] Three day Seminar on “FPGAs for Signal Processing and Embedded Applications”, at M S Ramaiah School Of Advanced Studies, Bangalore from 4th to 6th March 2010

[16] One week International workshop on “Image Processing” Indo-Us Engineering Faculty leadership institute from 5th to 9th July 2010, at MITS, madanapalle

[17] Two day Seminar on “VLSI Design-Trends and Tools”, at SKU Anantapur, March 19-20, 2006

[18] Two day workshop on “Embedded Systems”, at Deccan college of Engineering & Technology, Hyderabad, 9th & 10th January 2006

6. No of projects guided B.tech/M.tech

M. Tech: 09

B. Tech: 21

7. Book Published

Mahesh, “Feature Based Image Registration and Mosaicing” LAP LAMBERT Academic Publishing (August 3rd, 2015), ISBN 978-3-659-75759-4

8. Online Course

NPTEL Online certification “Microprocessors and microcontroller” successfully completed with Elite grade

9. Memberships in Professional Bodies:

1. Life Member in ISTE, NEW DELHI **LM 70354**



Dr M Mahesh

Professor of electronics and communications engineering,

Microprocessor
Microcontrollers
Digital systems
Image processing

	All	Since 2015
Citations	34	25
h-index	3	3
i10-index	1	1

TITLE	CITED BY	YEAR
Automatic image mosaic system using steerable Harris corner detector Mahesh, MV Subramanyam Machine Vision and Image Processing (MVIP), 2012 International Conference on ...	16 *	2012
Image mosaic using speeded up robust feature detection Mahesh, M Y Image 1 (3)	9 *	2012
Feature Based Image Mosaic Using Steerable Filters and Harris Corner Detector Maheh, m v subramanyam I.J. Image, Graphics and Signal Processing 5 (6), 9-15	6 *	2013
Image Mosaic Using FAST Corner Detection Mahesh, SM Nigar	2	2012
INVARIANT CORNER DETECTION USING STEERABLE FILTERS AND HARRIS ALGORITHM Mahesh, MV Subramanyam Signal & Image Processing 3 (5)	1	2012
RECOVERY OF DOCUMENT TEXT FROM TORN FRAGMENTS USING IMAGE PROCESSING Dr.Mahesh INTERNATIONAL JOURNAL OF ENGINEERING SCIENCES & RESEARCH TECHNOLOGY 5 (3 ...		2016
PERFORMANCE EVALUATION OF CORNER DETECTORS: A SURVEY Mahesh, R Sekhar K		2013
Feature based image registration using steerable filters and Harris algorithm Mahesh, S MV Communication and Computing (ARTCom2012), Fourth International Conference on ...		2012
Corner Detection Enhancement Using Steerable Filters Mahesh, DMV Subramanyam IJSER 3 (8)		2012
SIFT Based Image Mosaic algorithm Mahesh, KS R.Nagendra International Journal of Communications & Engineering 3 (1), 9-15		2012
Corner Detection using Curvelet and Harris Algorithm Mahesh, MV Subramanyam IJCST 3 (1)		2012
Non-Subsampled Contourlet Transform for Edge Detection Performance Mahesh, MV Subramanyam International Journal of Computational Intelligence Research 7 (3), 311-317		2011

