



**SHRAVANI CHIRUMALLA**

B.Tech, M.Tech (CS)

**Assistant. Professor**

Electrical & Electronics Engineering

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**Areas of Interest:**

- Power systems Smart Grids
- Power Electronics

### **Educational and Professional qualification**

- **Academic qualification**

- M.Tech in Control Systems, Aurora's Research and Technological Institute, JNTUH with First class with Distinction (2012-2014)
- B.Tech in Electrical and Electronics Engineering, Vaagdevi College of Engineering, JNTUH with First class (2006-2010)

- **Professional Experience**

- Working as an Assistant. Professor in the Department of EEE, TKRCET since July 2017.
- Worked as an Assistant Professor in the Department of EEE in Aurora's Research and Technological Institute, Warangal, Telangana. Period: June 20, 2011 to Nov, 2015

### **Workshops Attended:**

- Attended a workshop conducted by Tkr College of Engineering and Technology in the year 2018.

- Attended Faculty Development Programme conducted by CMR College of Engineering and technology in the year 2017.
- Attended Faculty Development Programme conducted by CristuJyothi Institute of technology and science in the year 2012
- Attended Faculty Development Programme conducted by Ramappa Engineering College in the year 2011.
- Participated in TECHNOPRENEUR PROMOTION PROGRAMME 2009 organized by NIT Warangal

#### **NPTEL coursework's:**

1. Completed a 8-week course work on **“Introduction to Smart Grid”** from June to Dec, 2019

#### **List of paper publishing's:**

1. “Performance of a novel seven level cascaded H-bridge Inverter with level shifted PWM techniques, IJR, Vol.No VIII Issue No VI, June 2019
2. "An Enhanced Current Control Approach for Grid Connected DG Unit with Closed Loop Power Control" in **IOJETR**.
3. "Leaky LMS Algorithm for DSTATCOM" in **AURORAIJC** Vol.No.2 Issue No.1, June 2015
4. "High Improved Instantaneous Current Control for High Bridge DC–DC Converter" in **AURORAIJC**. Vol.No.2 Issue No.1, June 2015
5. "Control Algorithm for Conductance estimation using Neural Network Controller for a DSTATCOM IN **AURORAIJC** Vol.No.2 Issue No.1, June 2015