

**Name : Y.PRIYANKA**

**Designation : Assoc.Prof**

**Qualification : M.Tech ( Ph.D )**

**Experience : 9 Years**

**Area of Interest: Wireless Communication**

**E- Mail id : [priyankayerrabelly@tkrcet.com](mailto:priyankayerrabelly@tkrcet.com)**



**Subject taught/Teaching:** Analog Communications  
Digital Communications  
Pulse and Digital Circuits  
Switching Theory and Logic Design  
Linear and Digital IC Applications

### **Education Qualification**

**( Ph.D ) - 2018 GITAM UNIVRSITY**

**M.Tech (Embedded Systems),2008-2010 Department of Electronics and Communication Engineering, SRTIST, JNTUH, Telangana**

**B.Tech (Electronics and Communication Engineering), 2004-2008 ,SRTIST, JNTUH Telangana.**

### **Employment record**

<b>University / College</b>	<b>Designation</b>	<b>Year</b>
TKRCollege of Engineering.& Technology Hyderabad	Associate Professor	1 <sup>st</sup> July2010 to till date

### **List of publications**

#### **International Journal: 3**

1). Published paper in 2018 IJSRSET entitled “**Analysis of Optimization Techniques for Low Power VLSI**” Design Volume 4 ,Issue 4, April-2018.

2). Published paper in International Journal of Research system entitled **“A Novel Design and Implementation of Hybrid Lut/Multiplexer For Fpga Logic Architectures”** e-ISSN: 2348-6848, p-ISSN: 2348-795X Volume 04, Issue-17,December 2017.

3).Published paper in International Journal of Scientific Engineering and Technology Research entitled **“Design of Arbiter with Effective Arbitration Logic for DMA Controller in AMBA Bus”** e-ISSN:2319-8885, vol.02,ISSUE.08, During August -2013.

### **Faculty Development Programs attended:**

1. One week Faculty Development Programme on **“Advanced CMOS VLSI”** organized by E & ICT Academy,NIT Warangal, From 3<sup>rd</sup> to 8<sup>th</sup> December,2018.

2. One week Faculty Development Programme on **“Signal Processing for 5G Wireless Communications”** at IARE , Dundigal ,From 12<sup>th</sup> to 17<sup>th</sup> November,2018.

3. One week Faculty Development Programme on **“Design of Experiments Using Taguchi Approach”** at TKRCET , Hyderabad, November,2018.

4. AICTE Sponsored Two week national level Faculty Development Programme on **“Speech, Image & Video Processing Techniques , Analysis & Applications”** at Mallareddy Engineering college for women, from6<sup>th</sup> to 18<sup>th</sup> November 2017.

### **Workshops attended:**

1. Attended workshop on “eSim- September,2019

2. Attended workshop on “HOME AUTOMATION WITH IOT USING RASPBERRY PI”,2017.

3. Attended workshop on “PCB DESIGN AND FABRICATION”, 2017.

4. Attended workshop on “INTRODUCTION TO PYTHON”,2017.

5. Attended workshop on “EMBEDDED SYSTEMS AND ROBOTICS”,2016.

6. Attended workshop on “ARM BASED EMBEDDED PROCESSING”,2015.