R N S KALPANA

B TECH M TECH

ASSISTANT PROFESSOR

Electronics and Communications Engineering

Areas of interest:

1. VLSI
2. Communications
3. Digital Electronics
4. Artificial inteligence

EDUCATIONAL & PROFESSIONAL:

Academic Qualification:

- B.Tech in Electronics and Communication Engineering, JNTU (2007).
- M.techG & TECH in VLSI System Design (ECE), Aurora College Of Engg & Tech (2010).

Paper publications:

1. Design of low power colum by[pass multiplier using FPGA 2550-3005 . IJCER VOLUME 3, ISSUE 2, FEBRUARY 2013.
2. Imge segmentation using neural VLSI system. 2329- 6726.IJESI VOL 6, ISSUE 11, NOV 2017 .
3. Computational primitive in neuromorphic VLSI system. 2329- 6726.IJESI VOL 6, ISSUE 11, NOV 2017 .

4. Survey on Fault Detection and Diagnosis Using Neural Network in WBAN International Journal of Engineering & Technology, 7 (2.20) (2018) 346-349.

Participated:

- Participated in five days FDP on "Oppurtunities, Challenges and Research Trends in wireless Sensor Networks" held at University college of engineering ,Osmania university from 11th to 15th December , 2017.

- Participated workshop on "Embedded systems and IOT- the opportunities" at ISM University in November, 2017.

- Participated workshop on "PCB Design And Fabrication" held on 25th& 26th October, 2017.

- Participated workshop on "Introduction To Python" held on 14th October, 2017.

- Participated workshop on "Home Automation With IOT Using Raspberry Pi" held on 18th&19th August, 2017.

- Participated workshop on "Embedded System and Robotics" held on 21st -23rd July 2016.

Teaching:

Pulse And Digital Circuits, analog electronics, ELECTRONIC CIRCUITS

Electronics and devices.