

J.SRAVANTHI

M.TECH

ASSISTANT PROFESSOR

Electronics & Communication Engineering

Area of Interest:

- 1. Microwave Engineering
- 2. VLSI
- 3. Communications

Educational & Professional:

Academic Qualifications:

M.Tech. in VLSI-System Design, Kshatriya College of Engineering, Armoor (2010-2013)

B. Tech. in Electronics and Communication Engineering, Kshatriya College of Engineering, Armoor (2003-2007)

Professional Experience:

At TKR College of Engineering and Technology

- Assistant Professor, Hyderabad (2015 – Till date).

Vijay Institute of Technology and Science

- Assistant Professor, Kamareddy (2008 - 2015).

Publications:

- Cooperative communication in UWB OFDM system,IJESI, ISSN: 2319-6734, Vol6, Issue 11, Nov 2017.
- Improving computation complexity in DDSTBC using coefficient vectors for fading channels, IJESI, ISSN:2319-6734, Vol6, Issue 11, Nov 2017
- An efficient VLSI architecture for normal I/O order FFT design, IJEEC, ISSN: 2048-1069 Vol 21, Issue 8, Nov 2017.
- Hybrid driver safety, vigilance and security system for vehicle using GSM module,IJR ISSN:2236-6124,VOL 8, ISSUE 4,April 2019.

• Design and implementation of MAC unit using fault tolerant reversible logic gates, IJR, ISSN: 2236-6124 Vol 8, Issue 4, April 2019.

Workshops:

- Attended workshop on "Arm Based Embedded Processing",2015.
- Attended training program on "Data Networking Routing & Switching",2015.
- Attended workshop on "Embedded Systems And Robotics",2016.
- Attended workshop on "Advanced Electromagnetic Engineering", GIAN, organized by Osmania University, 2017
- Attended workshop on "PCB design and fabrication", 2017.
- Attended workshop on "Home Automation with IoT using Raspberry Pi",2017.
- Attended workshop on "Introduction to Python",2017.

Subjects Handled (in UG level):

- Microwave Engineering(MWE)
- Electromagnetic waves and transmission lines(EMTL)
- Antennas and wave propagations(AWP)
- Satellite communications(SC)
- Data communication systems(DCS)
- Digital Signal Processing(DSP)
- Analog Communication(AC)

Project/Research Guidance:

Student Name	Title	Year
P.Sushma	Design And Implementation Of Mac Unit Using Fault Tolerant Reversible	2019
	Logic Gates	
Md.Khaja Naveed	Hybrid driver safety, vigilance and security system for vehicle using GSM	2019
	Module	
S.Dattatri Reddy	An efficient VLSI architecture for normal i/o order FFT design	2018
K.Shiva	The range less- Robo (A Robot with no limit and infinite range)	2017
A.Rakesh	Agricultural Field Motor Control By Using GSM	2016