



## **B. SWAPNA**

B.TECH, M.TECH

ASSISTANT PROFESSOR

Electronics & Communication Engineering

### **Areas of interest:**

1. VLSI
2. EMBEDDED SYSTEMS
3. COMMUNICATIONS

### **EDUCATIONAL & PROFESSIONAL:**

#### **Academic Qualification:**

- B.Tech in Electronics and Communication Engineering, Bhoj Reddy Engineering College For Women (2010).
- M.Tech in VLSI System Design (ECE), Vignan Institute Of Technology And Science (2012).

#### **Paper publications:**

1. B. Swapna, S. Sai Sree Andal. Implementation of Scientific Calculator Using CORDIC Algorithm on FPGA, Volume No: 3 , Issue No: 12 (November),PAGES 34-36,yuva engineers,2014.
2. B. Swapna, G. lavanya. Low Power High Speed Area Efficient Error tolerant Adder using Gate diffusion Input Method, Volume No: 9 , Issue No: 6(June),PAGES 2805-2810,IJMTAE,2019.

#### **Participated:**

- Participated a two day workshop on “Latest advances in Electronics and communication systems” held at TKRCET, in March-2013.
- Participated a three day workshop on “PCB design” held at TKRCET, in September 2013.
- Participated a Two day workshop on “Image Processing using MATLAB” held at TKRCET, in January 2014.
- Participated a Two day workshop on “Digital Image Processing using MATLAB “, in January , 2014.

- Participated in 3 day interactive workshop on “Intel Atom Embedded Training Program-3” held at JNTUH in March 2014.
- Participated one week FDP on “Modern teaching trends in Scientific and Technical Education” in July, 2014.
- Participated a Two day workshop on “Embedded and VLSI Design”, in August, 2014.
- Participated FDP on “Research awareness in Advanced VLSI Design” in September, 2014.
- Participated FDP on “Teaching Pedagogy, Programme Education objectives” in 2014.
- Participated in 2 day workshop on “Digital IC Design” in June, 2014.
- Participated Training Programme on “Data Networking, Routing and Switching “ in September, 2015.
- Participated in 1 day workshop on “Research Trends in VLSI & Embedded Systems “ in November, 2016.
- Participated in 3 day workshop on “Robotics” in July, 2016.
- Participated in 2 day workshop on “Home Automation with IoT using Raspberry Pi” in August, 2017.
- Participated in 2 day workshop on “Introduction to Python” held in October,2017.
- Participated in 2 day workshop on “PCB Design and Fabrication” in October,2017.
- Participated FDP on “Analog IC Design - Hands on Practice using Cadence Design Flow" held at JNTUH, in April 2019.

### Teaching:

Microprocessors and Microcontrollers, Analog Communications, Electronic Devices and Circuits, Embedded Systems, Data Communication system, Microcontroller Applications .