



**TKR COLLEGE OF ENGINEERING AND TECHNOLOGY
(AUTONOMOUS)**

(Sponsored by TKR Educational Society, Approved by AICTE, Affiliated by JNTUH,
Accredited by NBA & NAAC with 'A' Grade)



ELECTRICAL AND ELECTRONICS ENGINEERING

NEWS LETTER

Frequency: 6-months

EDITION -1

July-December 2022



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1. PREFACE

It gives us immense pleasure to present the latest edition of the Electrical and Electronics Engineering Department Newsletter. This publication serves as a vibrant platform to showcase the achievements, innovations, and activities of our students, faculty, and staff, while also highlighting recent developments in the ever-evolving world of Electrical and Electronics Engineering.

In this fast-paced technological era, the role of Electrical and Electronics engineers has become more critical than ever. From advancements in power systems and embedded technology to breakthroughs in automation and renewable energy, our department continues to stay at the forefront of academic excellence and research innovation.

This newsletter is a reflection of our collective efforts — featuring technical articles, project highlights, research contributions, industrial visits, workshops, guest lectures, and student accomplishments. It is a celebration of curiosity, creativity, and collaboration within our department.

We extend our heartfelt gratitude to the editorial team, contributors, and all those who supported the making of this issue. Their dedication and hard work have made this publication possible.

We hope this newsletter not only informs but also inspires readers to engage more deeply with the exciting field of Electrical and Electronics Engineering. May it serve as a testament to our department's commitment to learning, growth, and innovation.

Happy Reading!

2. MESSAGE FROM MANAGEMENT



**SRI TEEGALA KRISHNA REDDY
CHAIRMAN**



**SRI TEEGALA HARINATH REDDY
SECRETARY**



**SRI TEEGALA AMARNATH REDDY
TREASURER**

"Unlock the code to innovation with our branch of Electrical and Electronics Engineering! Our brilliant minds are shaping the future, one algorithm at a time. Join the journey of discovery, where bytes become breakthroughs. Embrace the power of knowledge, where tomorrow's tech leaders are born today. Dive in to the digital revolution!"

3. MESSAGE FROM THE PRINCIPAL



Dr. D.V. RAVI SHANKAR

PRINCIPAL-TKRCET

"As the College Principal, I'm thrilled to witness the boundless potential within our Electrical and Electronics Engineering realm. Here, innovation isn't just a word; it's a lifestyle. Embrace the fusion of logic and creativity, for it births groundbreaking solutions. Our community breeds pioneers, shaping tomorrow's tech landscape. Let curiosity be your compass, resilience your fuel, and knowledge your arm or. Remember, every line of code is a step toward progress. Together, let's amplify our impact, leaving an indelible mark on the digital frontier. Your journey here isn't just about learning; it's about pioneering the future. Seize every opportunity, and let's thrive together!"



4. EDITORIAL

Dear Enthusiasts,

"Welcome to the pulse of innovation! Within these pages, discover the collaborative synergy between our esteemed faculty and visionary student coordinators. Together, we traverse the ever-evolving landscape of Computer Science Engineering, nurturing creativity, and fostering excellence. Our journey isn't just about acquiring knowledge; it's about applying it to redefine boundaries. From groundbreaking research to dynamic events, witness our collective passion for innovation. We invite you to join this vibrant community, where expertise meet enthusiasm, and together, we sculpt the future. Embrace the spirit of exploration, for here, possibilities are limitless, and brilliance knows no bounds."

FACULTY COORDINATORS

**Dr. K. Raju
(Associate Professor)**

**Mr. P. Veeraraghava Reddy
(Assistant Professor)**

STUDENT COORDINATORS

Vivek Nani

5. ABOUT THE DEPARTMENT



:

Dr. K. RAJU
HEAD OF DEPARTMENT-EEE

"Welcome to the vibrant hub of technological evolution at TKR College of Engineering and Technology! Established in 2002, The Department of Electrical Engineering, established in 2002, initially offered a B. Tech program in Electrical & Electronics Engineering. To diversify and provide industry-oriented training at the postgraduate level, the department introduced M. Tech courses in Power Electronics in 2010. Currently, Research efforts are focused on various areas within Electrical Engineering, such as Power Systems, Power Electronics and Drives, Power Converters, High Voltage Engineering, Control Systems, Artificial Intelligence Techniques, Renewable Energy Resources, Electric Vehicles, and software tools like ETAP, MATLAB, Ansys, and Python.



6. ACADEMIC EXCELLENCIA

"Embarking on the journey of academic excellence, our stellar achievers have illuminated the path to success with their unwavering dedication and brilliance. Join us in celebrating the remarkable achievements of our top scholars, whose relentless pursuit of knowledge has set a soaring standard in our academic community."

BACHELOR OF TECHNOLOGY

The following students have a topper an EEE Dept.

S. No	Year and Sem.	Roll No	Name of the student	CGPA
1	4-2	19K91A0201	ARCH ROOPA	8.83

7. NEWS AND ACHEIVEMENTS

Ayudha Puja and Bathukamma was celebrated on 30/10/2022; Durga Devi Mandapam was specially decorated the visitor have appreciated the uniqueness of arrangements.





FACULTY DEVELOPMENT PROGRAMMES

1. Mr P. Veeraraghava Reddy Asst. Professor was attended FDP on A six days faculty Devoplmenet program on Recent advancements in power system on 28-11-22 to 03-12-22

2. Mr P. Veeraraghava Reddy Asst. Professor was attended One week online international FDP on Trends & Challenges in the development of EV & Hybrid ev(series-2)on 14-11-2022 to 18-11-2022



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1. I G. Jagadesh Reddy Recently received for my participation in the NPTEL titled **Deep learning**, which took place on jul-oct 2022. I receiving this certificate have been an enriching experience.



Elite

NPTEL Online Certification

(Funded by the MoE, Govt. of India)



This certificate is awarded to
JAGADESHWAR REDDY GOGU
for successfully completing the course



Deep Learning

with a consolidated score of **75** %

Online Assignments	19.72/25	Proctored Exam	55/75
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Total number of candidates certified in this course: **1474**

Devendra Jali hal

Prof. Devendra Jali hal
Chairperson,
Centre for Outreach and Digital Education, IITM

Jul-Oct 2022
(12 week course)

Prof. Andrew Thangaraj

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras



Indian Institute of Technology Madras



Roll No: NPTEL22CS124S63693770

To validate the certificate



No. of credits recommended: 3 or 4



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1. A one day seminar on CAREER OPPORTUNITIES THROUGH GATE EXAM AND PREPARATION STRATEGY by Mr. B. SURENDRA REDDY (Subject Expert at OHM Institute, M. Tech from IITB) On 19-12-2022 have been conducted for EEE Dept.



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Seminar on

**CAREER OPPORTUNITIES THROUGH GATE EXAM AND
PREPARATION STRATEGY**

ORGANISED BY

DEPARTMENT OF EEE

IN ASSOCIATION WITH



**OHM INSTITUTE
HYDERABAD**





19-12-2022



2.00pm

Resource Person

Mr. B. SURENDRA REDDY
(Subject Expert at OHM Institute, M. Tech from IITB)

Coordinator	HOD	Principal
Dr. K. Raju	Dr. S. Narasimha	Dr. D.V. Ravi Shankar



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1. Industrial Visit to KKR Transformers (KKR), Nacharam ,Hyderabad, Telangana, India by B-Tech EEE, Students along with faculty's on 12th November 2022.





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STUDENT BULLETIN

One day seminar on A guidance Program on Career Opportunities After B. Tech by Faculty of ACE Engineering Academy . Mr S . MM TRINATH on 31-08-2021 have been conducted for B. Tech EEE Students.



1. Student of IV-II Roll No 20K95A0206 B.Kranthi Kumar with Gate Hall Ticket No: EE23S31406860 Secured 521 Rank And All India Rank 1233.



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Scorecard

Name of Candidate	BANDARI KRANTHI KUMAR
Parent's/Guardian's Name	SRINIVAS
Registration Number	EE23S31406860
Date of Birth	04-Jan-2002
Examination Paper	Electrical Engineering (EE)



B. Kranthi

GATE Score:	521	Marks out of 100:	35
All India Rank in this paper:	1233	Qualifying Marks*	General
Number of Candidates Appeared in this paper:	55292		EWS/OBC (NCL) SC/ST/PwD.
			25.0 22.5 16.6

Valid up to 31st March 2026

Mohitep
Prof. Preetam Kumar M. Mohite
Organizing Chairman, GATE 2023
on behalf of NCB-GATE, for MoE



1d1011c9edd3ed44e4a920aa046bda0d

* A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category for which valid category certificate, if applicable, is produced along with this score card.

General Information

The GATE 2023 score is calculated using the formula

$$\text{GATE Score} = S_q + (S_t - S_q) \frac{(M - M_q)}{(M_t - M_q)}$$

where,

M is the marks obtained by the candidate in the paper, mentioned on this GATE 2023 scorecard

M_q is the qualifying marks for general category candidate in the paper

M_t is the mean of marks of top 0.1% or top 10 (whichever is larger) of the candidates who appeared in the paper (in case of multi-session papers including all sessions)

S_q = 350, is the score assigned to M_q

S_t = 900, is the score assigned to M_t

In the GATE 2023 score formula, M_t is 25 marks (out of 100) or $\mu + \sigma$, whichever is greater. Here μ is the mean and σ is the standard deviation of marks of all the candidates who appeared in the paper.

Qualifying in GATE 2023 does not guarantee either an admission to a post-graduate program or a scholarship/ assistantship. Admitting institutes may conduct further tests and interviews for final selection.

Graduate Aptitude Test in Engineering (GATE) 2023 was organized by Indian Institute of Technology Kanpur on behalf of the National Coordination Board (NCB) – GATE for the Department of Higher Education, Ministry of Education (MoE), Government of India.