



**TKR COLLEGE OF ENGINEERING AND TECHNOLOGY
(AUTONOMOUS)**

(Sponsored by TKR Educational Society, Approved by AICTE, Affiliated by JNTUH,
Accredited by NBA & NAAC with 'A' Grade)



ELECTRICAL AND ELECTRONICS ENGINEERING

NEWS LETTER

Frequency: 6-months

EDITION -2

January-June 2022



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1. PREFACE

It gives us immense pleasure to present the latest edition of the Electrical and Electronics Engineering Department Newsletter. This publication serves as a vibrant platform to showcase the achievements, innovations, and activities of our students, faculty, and staff, while also highlighting recent developments in the ever-evolving world of Electrical and Electronics Engineering.

In this fast-paced technological era, the role of Electrical and Electronics engineers has become more critical than ever. From advancements in power systems and embedded technology to breakthroughs in automation and renewable energy, our department continues to stay at the forefront of academic excellence and research innovation.

This newsletter is a reflection of our collective efforts — featuring technical articles, project highlights, research contributions, industrial visits, workshops, guest lectures, and student accomplishments. It is a celebration of curiosity, creativity, and collaboration within our department.

We extend our heartfelt gratitude to the editorial team, contributors, and all those who supported the making of this issue. Their dedication and hard work have made this publication possible.

We hope this newsletter not only informs but also inspires readers to engage more deeply with the exciting field of Electrical and Electronics Engineering. May it serve as a testament to our department's commitment to learning, growth, and innovation.

Happy Reading!



2. MESSAGE FROM MANAGEMENT



**SRI TEEGALA KRISHNA REDDY
CHAIRMAN**



**SRI TEEGALA HARINATH REDDY
SECRETARY**



**SRI TEEGALA AMARNATH REDDY
TREASURER**

"Unlock the code to innovation with our branch of Electrical and Electronics Engineering! Our brilliant minds are shaping the future, one algorithm at a time. Join the journey of discovery, where bytes become breakthroughs. Embrace the power of knowledge, where tomorrow's tech leaders are born today. Dive in to the digital revolution!"

3. MESSAGE FROM THE PRINCIPAL



Dr. D.V. RAVI SHANKAR

PRINCIPAL-TKRCET

"As the College Principal, I'm thrilled to witness the boundless potential within our Electrical and Electronics Engineering realm. Here, innovation isn't just a word; it's a lifestyle. Embrace the fusion of logic and creativity, for it births groundbreaking solutions. Our community breeds pioneers, shaping tomorrow's tech landscape. Let curiosity be your compass, resilience your fuel, and knowledge your arm or. Remember, every line of code is a step toward progress. Together, let's amplify our impact, leaving an indelible mark on the digital frontier. Your journey here isn't just about learning; it's about pioneering the future. Seize every opportunity, and let's thrive together!"



4. EDITORIAL

Dear Enthusiasts,

"Welcome to the pulse of innovation! Within these pages, discover the collaborative synergy between our esteemed faculty and visionary student coordinators. Together, we traverse the ever-evolving landscape of Computer Science Engineering, nurturing creativity, and fostering excellence. Our journey isn't just about acquiring knowledge; it's about applying it to redefine boundaries. From groundbreaking research to dynamic events, witness our collective passion for innovation. We invite you to join this vibrant community, where expertise meets enthusiasm, and together, we sculpt the future. Embrace the spirit of exploration, for here, possibilities are limitless, and brilliance knows no bounds."

FACULTY COORDINATORS

**Dr. K. Raju
(Associate Professor)**

**Mr. P. Veeraraghava Reddy
(Assistant Professor)**

STUDENT COORDINATORS

V. Rakesh Tadav

5. ABOUT THE DEPARTMENT



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Dr. K. RAJU
HEAD OF DEPARTMENT-EEE

"Welcome to the vibrant hub of technological evolution at TKR College of Engineering and Technology! Established in 2002, The Department of Electrical Engineering, established in 2002, initially offered a B. Tech program in Electrical & Electronics Engineering. To diversify and provide industry-oriented training at the postgraduate level, the department introduced M. Tech courses in Power Electronics in 2010. Currently, Research efforts are focused on various areas within Electrical Engineering, such as Power Systems, Power Electronics and Drives, Power Converters, High Voltage Engineering, Control Systems, Artificial Intelligence Techniques, Renewable Energy Resources, Electric Vehicles, and software tools like ETAP, MATLAB, Ansys, and Python.



6. ACADEMIC EXCELLENCIA

"Embarking on the journey of academic excellence, our stellar achievers have illuminated the path to success with their unwavering dedication and brilliance. Join us in celebrating the remarkable achievements of our top scholars, whose relentless pursuit of knowledge has set a soaring standard in our academic community."

BACHELOR OF TECHNOLOGY

- Vadiyalasai Nanda Kumar from IV Year II Semester bearing Roll Number 18K91A0285 ,has topped the class with an impressive CGPA of 9.2



7. NEWS AND ACHEIVEMENTS

FACULATY DEVELOPMENT PROGRAMMMES

1. Mr P. Veeraraghava Reddy Asst. Professor was attended FDP on Recent Trends in Electric Vechivle and smart grid with Renewable Energy Technologies for Sustainable Development on 06-06-2022 to 10-06-2022
2. Mr S. Manohar Reddy Asst. Professor was attended Smart Grids and Micro Grids in Indian Cotext on 20-06-2022 to 24-06-2022



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STUDENTBULLETIN

- Student of IV-II Roll No 18K91A0285 V. Sai Nandha Kumar with Gate Hall Ticket No: EE23S31405197 Secured 561 Rank And All India Rank 1696.

GATE 2023		Scorecard			
Name of Candidate	VADIYALA SAI NANDA KUMAR				
Parent's/Guardian's Name	SREENIVASA RAO				
Registration Number	EE23S31405197				
Date of Birth	28-Sep-2000				
Examination Paper	Electrical Engineering (EE)				
GATE Score:	481	Marks out of 100:	32.67		
All India Rank in this paper:	1696	Qualifying Marks*	General	EWS/OBC (NCL)	SC/ST/PwD
Number of Candidates Appeared in this Paper	55292		25.0	22.5	16.6
Valid up to 31 st March 2026					
Prof. Probal Kumar M. Mohite Organizing Chairman, GATE 2023 on behalf of NCB-GATE, for MoE				* A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category for which valid category certificate, if applicable, is produced along with this score card.	
General Information					
The GATE 2023 score is calculated using the formula					
$\text{GATE Score} = S_q + (S_t - S_q) \frac{(M - M_q)}{(M_t - M_q)}$					
where, M is the marks obtained by the candidate in the paper, mentioned on this GATE 2023 scorecard M _t is the qualifying marks for general category candidate in the paper M _q is the mean of marks of top 0.1% or top 10 (whichever is larger) of the candidates who appeared in the paper (in case of multi-session papers including all sessions) S _t = 350, is the score assigned to M _t S _q = 900, is the score assigned to M _q					
In the GATE 2023 score formula, M _t is 25 marks (out of 100) or $\mu + \sigma$, whichever is greater. Here μ is the mean and σ is the standard deviation of marks of all the candidates who appeared in the paper.					
Qualifying in GATE 2023 does not guarantee either an admission to a post-graduate program or a scholarship/assistantship. Admitting Institutes may conduct further tests and interviews for final selection.					
Graduate Aptitude Test in Engineering (GATE) 2023 was organized by Indian Institute of Technology Kanpur on behalf of the National Coordination Board (NCB) – GATE for the Department of Higher Education, Ministry of Education (MoE), Government of India.					